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maintain virtual PCI device information

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Method and apparatus for address

translation on PCI bus over infiniband network

US Pat. 6658521 - Filed Jan 30, 2001 - International Business Machines Corporation

The TCA must **maintain** a translation table that maps the physical address to the... Another approach would be to force the **PCI device** or to post a **virtual** ...

Software transparent system and method for peer-to-peer message routing

US Pat. 6606679 - Filed Nov 28, 2001 - Intel Corporation

... header identifying a corresponding **device 218** as a **virtual PCI device 208** ...the switch 210 may comprise a memory to **maintain** routing **information** in a ...

Transmission of voice over an asynchronous network

US Pat. 6118763 - Filed Sep 19, 1997 - Inventions, Inc.

The header field 305 also contains other **information**, such as a Generic Flow ...be forwarded to a **device** on its bus (SCSA, **PCI**) by testing the **virtual** ...

Peripheral component interface with multiple data channels and reduced ...

US Pat. 6487628 - Filed Mar 31, 1999 - Compaq Computer Corporation

Once granted a data channel, a **PCI device** controller would program its own DMA... The TSN Manager 62 has two functions: **maintain** all **DCE TSN information**, ...

System for implementing a graphic address remapping table as a virtual ...

US Pat. 6195734 - Filed Jul 2, 1997 - Micron Technology, Inc.

The **PCI/ISA** bridge couples the **PCI** bus to an Industry Standard ArchitectureIn order to initialize and **maintain** individual **virtual** registers within the ...

System and method for managing bus numbering

US Pat. 6973525 - Filed Mar 19, 2002 - Dell Products L.P.

Once trap handler module 48 is disabled, the **virtual PCI** bridge disappears to... a separate hardware **device** may be interfaced with the **information** handling ...

Store and forward switch device, system and method

US Pat. 7251704 - Filed Aug 23, 2002 - Intel Corporation

13 "**PCI-to-PCI** Bridge Specification") to emulate or identify a "**virtual**"the switch 610 may comprise a memory to **maintain** routing **information** in a ...

Method for implementing a graphic address remapping table as a virtual ...

US Pat. 6192457 - Filed Jul 2, 1997 - Micron Technology, Inc.

The **PCI/ISA** bridge couples the **PCI** bus to an Industry Standard ArchitectureIn order to initialize and **maintain** individual **virtual** registers within the ...

System and method for managing bus numbering

US Pat. 7165136 - Filed Sep 13, 2005 - Dell Products L.P.

At step 52, a determination is made of whether the **virtual PCI** bridge **device** isenabled. For instance, if the BIOS 36 detects that **PCI** bus 20 is inactive, ...

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Method and apparatus for monitoring input/output ("I/O") performance in I/O ...

US Pat. 6381656 - Filed Mar 10, 1999 - Applied Microsystems Corporation

The I/O user interface 220 retrieves **information** from the **virtual** adapter ...DAT file 805 returns **device information** about only one TID on the IOR The TID ...

Method for identifying the original source **device** in a transaction request ...

US Pat. 6067581 - Filed Jan 20, 1998 - Micron Electronics, Inc.

The original source **device** could also be the **PCI** module 211, the host module ...and provides the requested **information**, allowing translation of the **virtual** ...

System and method for coupling peripheral buses through a serial bus using a ...

US Pat. 6418504 - Filed Jun 6, 2001 - National Instruments Corporation

Another popular I/O function in which **PCI** is used is in the area of instrumentation.An instrument is a **device** which collects data or **information** from an ...

System and method for managing bus numbering

US Pat. 6973525 - Filed Mar 19, 2002 - Dell Products L.P.

Once trap handler module 48 is disabled, the **virtual PCI** bridge disappears to... a separate hardware **device** may be interfaced with the **information** handling ...

Method and apparatus for controlling congestion in a network node

US Pat. 5867480 - Filed Sep 12, 1996 - Cabletron Systems, Inc.

10 15 ATM messages move through the network over **virtual** circuits (VCs), ...In this embodiment, the system bus 18 is a 32-bit **PCI** bus with Universal I/O. ...

Transaction credit control for serial I/O systems

US Pat. 6760793 - Filed Jul 29, 2002 - ISYS Technologies, Inc.

PCIX devices are required to be fully backward compatible with **PCI**, such thatwhenever a **PCI device** is installed on that bus segment, the PCIX devices on ...

Microprocessor having improved memory management unit and cache memory

US Pat. 6591340 - Filed Jun 10, 2002 - Hitachi, Ltd.

For each set and way there is stored the **virtual** tag of the address whose data is.... At step 300, an external **device**, in this example a **PCI-type device** ...

Method and apparatus for storing volumetric vital product data (VPD) in ...

US Pat. 6542939 - Filed Aug 10, 1999 - International Business Machines Corporation

If the **device** was a **PCI** Card, then, for example, 14 bytes for the **device** volume... 2) chose to implement only the '**virtual-solid**' volume parameters (in a ...

ATM switching with **virtual** circuit FIFO buffers

US Pat. 6414961 - Filed Jun 10, 1998 - NEC Corporation

In this case, as the **PCI** bus between the ATM communication 35 apparatus and ...given **virtual** channel, forming the payload data, cell header **information**, ...


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System direct memory access (DMA) support logic for PCI based computer system - all 3 versions »

N Amini, PM Bland, BF Boury, RG Hofmann, TJ Lohman - US Patent 5,450,551, 1995 - Google Patents

... with the I/O bridge, the sideband signals including **information** iden-tifying the bus size of the selected I/O **device** for which ... V_V PCI HOST 6R I DGE ...

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PCI split transactions utilizing dual address cycle - all 3 versions »

DM Bell - US Patent 5,594,882, 1997 - Google Patents

... function for the bridge units to **maintain** proper transaction ... necessary when operating in the standard **PCI** protocol mode ... the 32 bit address of the target **device**. ...

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Method and apparatus to enable insertion/ejection of a device in a computer system while maintaining ... - all 3 versions »

PD Shah, KC Ma, JA Hawkey, KJ Kotlowski - US Patent 6,141,711, 2000 - Google Patents

... In a network environment, it may be necessary to **maintain** operation of client ...

Secondary

bus controller 120 may reside as a **device** on primary **PCI** bus 104 ...

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Bus-to-bus pacing logic for improving information transfers in a multi-bus information handling ... - all 3 versions »

N Amini, A Kohli, GN Santos - US Patent 5,564,026, 1996 - Google Patents

... Computer systems or **information** handling systems typi-cally ... boundary (assuming, of course, that **PCI** arbitration guide ... do not force the **device** initiating the ...

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... for maintaining coherency for data transaction of CPU and bus device utilizing selective flushing ... - all 3 versions »

BM Davis, NJ Richardson, BN Fall... - US Patent 6,021,473, 2000 - Google Patents

... METHOD AND APPARATUS FOR **MAINTAINING** COHERENCY FOR DATA ... One popular **device** bus

standard is the Peripheral Com-ponent Interconnect (**PCI**) bus by ...

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... termination data for each target device and periodically terminating burst transfer to device ... - all 3 versions »

JR Lory, VK Pecone - US Patent 5,768,622, 1998 - Google Patents

... This operates to **maintain** the target's maximum burst ... the bus master receives a termination from the target **device**. ... together fast back-to-back **PCI** cycles while ...

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Methods and structure to maintain raid configuration information on disks of the array - all 3 versions »

DR Humlicek, JR Kloeppner, GG Phillips, CW Rink - US Patent 5,822,782, 1998 - Google